Claim 3 (amended). The circuit configuration according to claim 1, which comprises a fourth capacitor connected between said second connection of said third parallel LC element and the fixed reference-ground potential.

B' Claim 4 (amended). The [bandpass filter] circuit configuration according to claim 1, wherein said bandpass filter has a further capacitor having a first terminal connected to a node between said second capacitor and said inductor and a second terminal connected to the fixed reference-ground potential.

Enter The Following New Claims:

--18. A circuit configuration, comprising:

an AC voltage input terminal and an AC voltage output terminal;

B2 a plurality of frequency domain filter paths defined between said AC voltage input terminal and said AC voltage output terminal, and connected in parallel between a common first node and a common second node both coupled to a DC voltage connection;

each of said frequency domain filter paths containing at least one bandpass filter connected in series with a first diode and

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a second diode connected in opposite forward direction from said first diode;

each of said frequency domain filter paths containing a switching unit for switching said first and said second diode in said frequency domain filter path;

a third diode having a first terminal connected to said first node and a fourth diode having a first terminal connected to said second node of said frequency domain filter paths for connecting a respective cathode of said third diode and of said fourth diode to anodes of said first diodes and said second diodes, respectively;

a load-dependent DC voltage source having a first connection and a second connection; and

said third diode and said fourth diode each having a second terminal respectively connected to said first connection and said second connection of said load-dependent DC voltage source.

8. The circuit configuration according to claim 7, wherein said first diode, said second diode, said third diode, and said fourth diode are PIN diodes.

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10. The circuit configuration according to claim 8, wherein each of said switching units includes:

a first electrical resistor and a second electrical resistor respectively connected, via a first terminal thereof, to said input and to said output of an associated one of said frequency domain filters, and to one another via a second terminal thereof;

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an on/off switch having a first terminal connected between said first electrical resistor and said second electrical resistor, and a second terminal connected to a fixed reference-ground potential; and

a capacitor having a first terminal connected between said first electrical resistor and said second electrical resistor, and a second terminal connected to the fixed reference-ground potential. -

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